FIG. 1

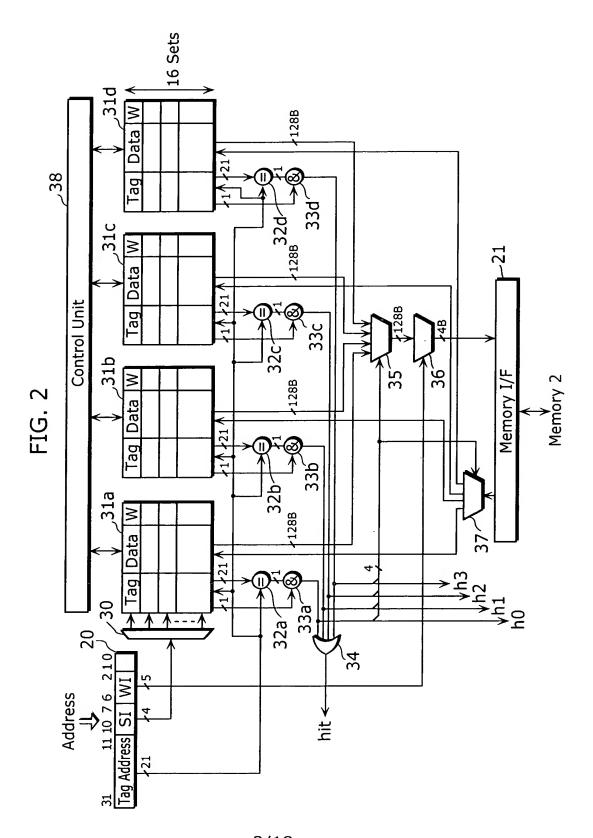
Processor

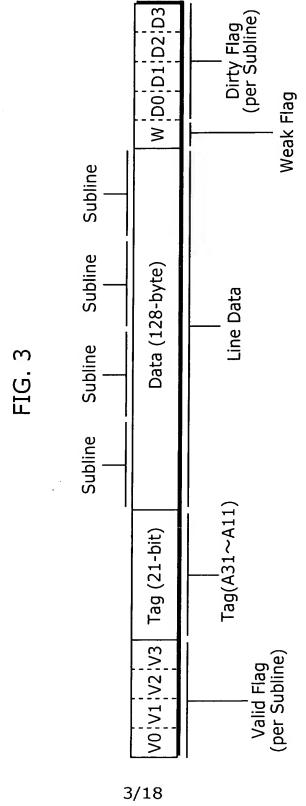
D

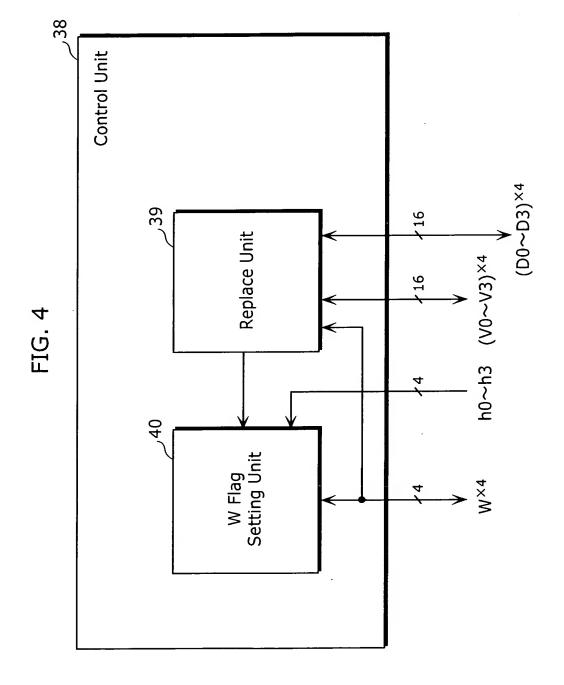
Cache

Memory

D







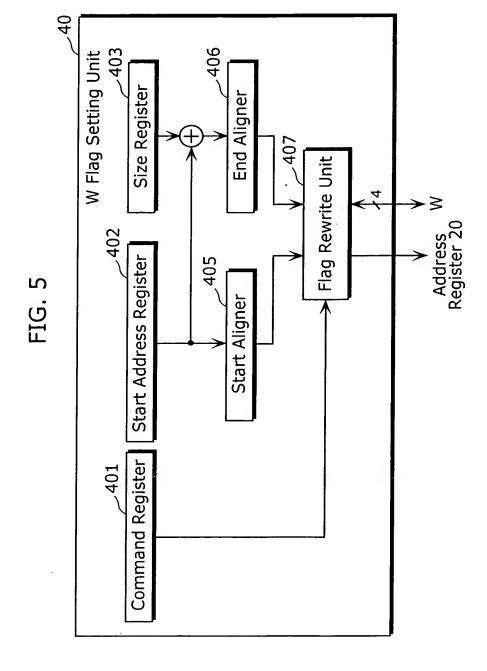


FIG. 6A

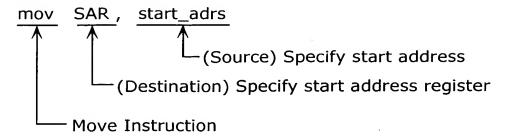


FIG. 6B

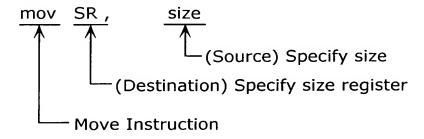


FIG. 6C

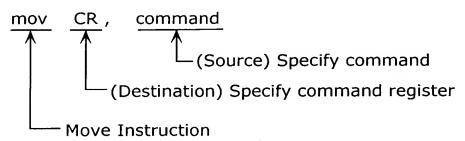
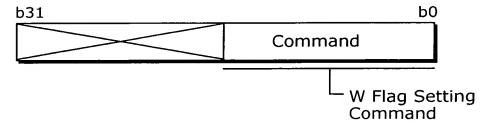


FIG. 6D



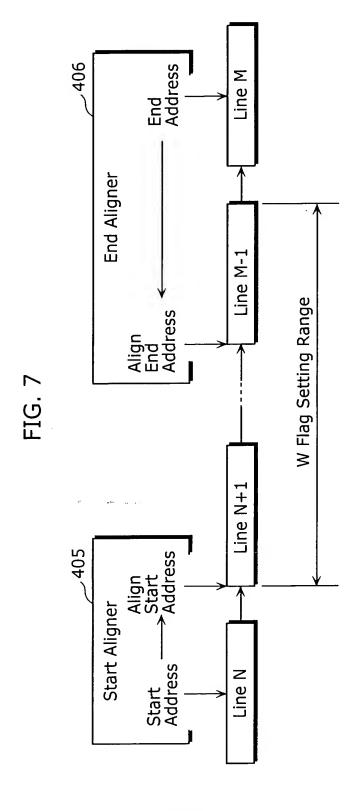


FIG. 8

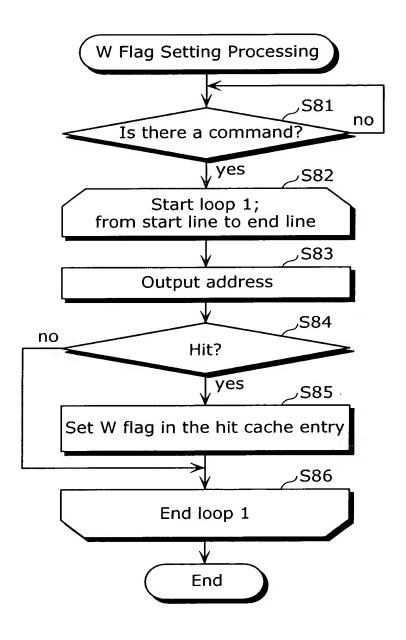
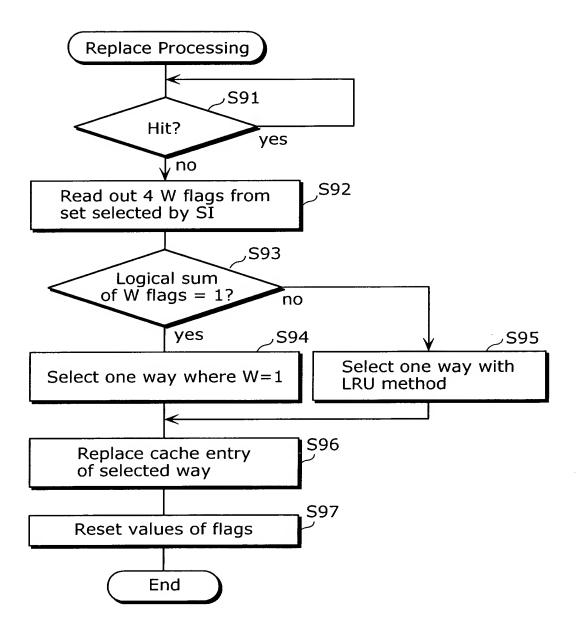
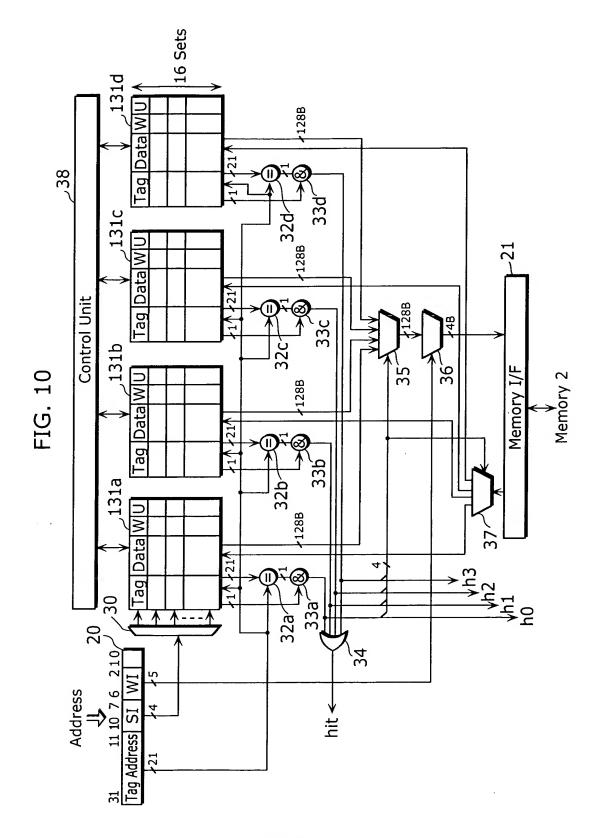
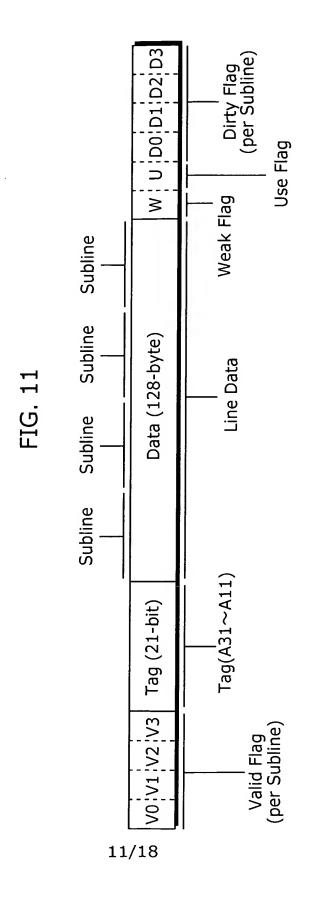
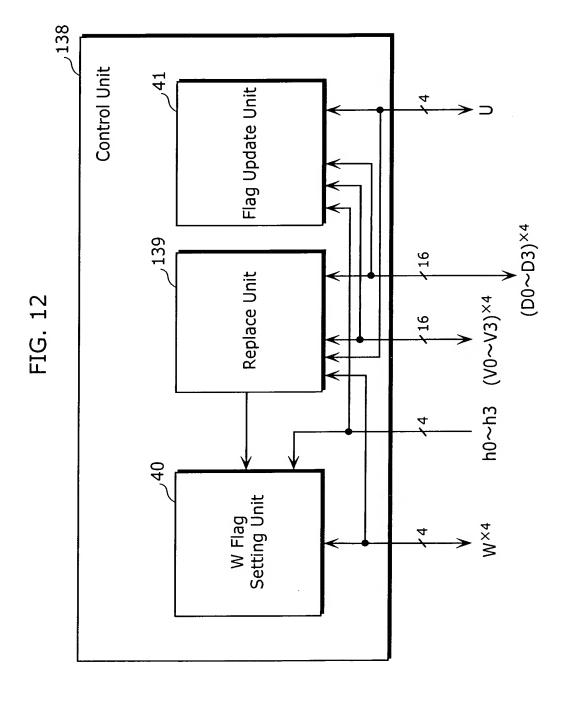


FIG. 9









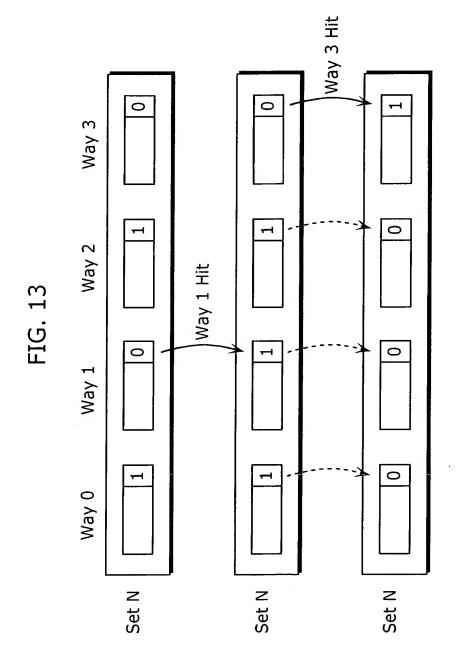


FIG. 14A

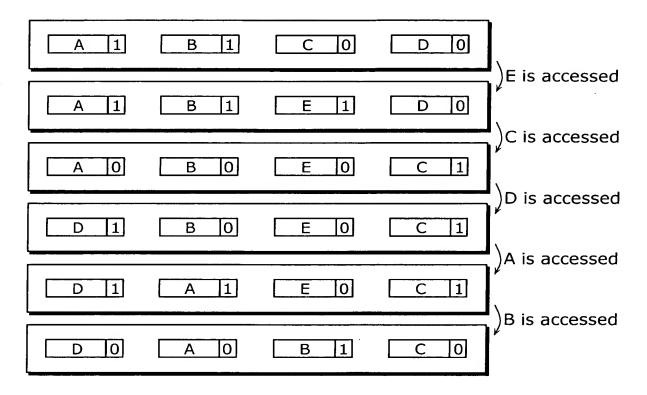


FIG. 14B

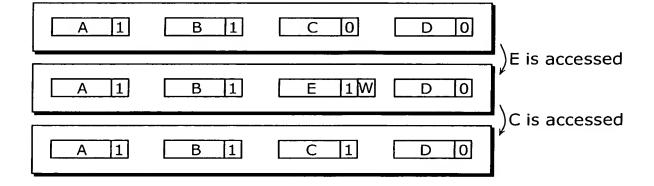


FIG. 15

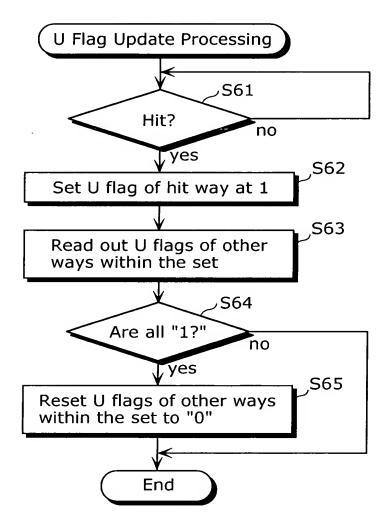


FIG. 16

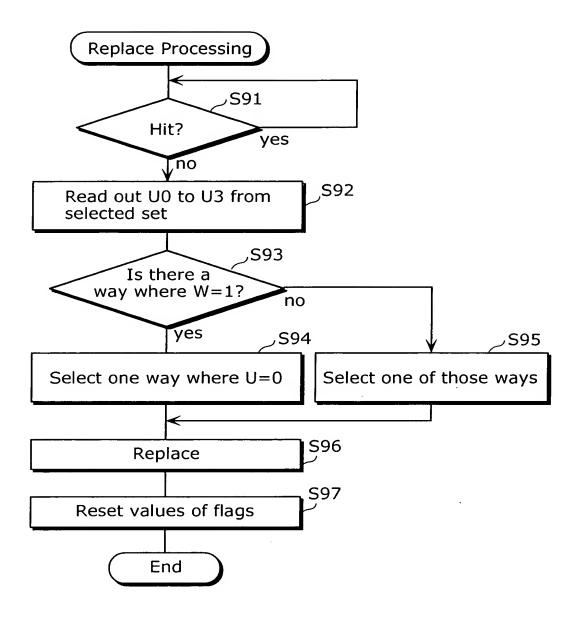


FIG. 17

